

ABSTRACT

An image signal processing device has LSI (21) including ROM access control circuit (22) for holding data for controlling an operation of a video signal processing unit, and flush ROM (23) that is disposed outside LSI (21), holds
5 control data to be transmitted to ROM access control circuit (22), and allows data read to be controlled by ROM access control circuit (22). The data transferred between flush ROM (23) and ROM access control circuit (22) has data that must be updated every field and data that does not need to be updated every field. The data is transferred in a vertical blanking time period
10 of the video output data. This configuration allows certain transfer of required data using the vertical blanking time period.